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2 HANDS-ON VHDL: AN INTRODUCTION, errata  
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4 1) On pages 26, 28, and 36 I refer to ".xcf" files. These were the original Xilinx  
constraint files, and are now obsolete, replaced by ".xdc" files, which are included in  
the actual project folders.

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6 2) it has been pointed out that a better description of programming the Digilent Basys  
3 board's FPGA than that provide on pages 26-35 of the book is the "Getting Started  
with the Vivado and Basys-3" video found on the Digilent website.

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8 3) on pages 109 and 113 the operation descriptions for the two boards are swapped.  
E.g., the picture and associated descriptions following page 108 should be that of page  
113, and the picture and associated descriptions following page 112 should be that of  
page 109.

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10 4) on page 109:  
11 the fourth point indicates "(associated with displays "A" and "C")", but should  
read "(associated with displays "A" and "B")";  
12 the fifth point indicates "(associated with displays "B" and "D")", but should read  
"(associated with displays "C" and "D")".

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15 (Thanks to Jerry for communicating these).